

Amendments to the Claims (by Examiner's Amendment):

1. (Cancelled)

2. (Cancelled)

3. (Cancelled)

4. (Currently amended) The matrix display device according to ~~claim 1~~
claim 17, wherein the display element comprises an Organic Light Emitting Diode.

5. (Cancelled)

6. (Cancelled)

7. (Currently amended) A display apparatus, comprising:

a matrix display device as claimed in ~~claim 1~~ claim 9,

a data driver circuit for applying said data signal to a data terminal of the addressing element; and

a selection driver circuit for applying a selection signal to said ~~means for independent voltage control~~
second selection line.

8. (Previously amended) The display apparatus according to claim 7, wherein said independent voltage control means comprise duty cycle control means.

9. (Previously added) A control circuit for controlling a display element of a pixel in a pixel array, the control circuit comprising:

- a drive transistor for driving the display element;
- an address transistor for sending a data signal to the drive transistor, a gate of the address transistor being connected to a first selection line;
- a capacitor connected to a gate of the drive transistor; and
- a photosensitive transistor coupled to the capacitor for regulating a charge stored on the capacitor in accordance with light emitted from the display element, a gate of the photosensitive transistor being connected to a second selection line for providing independent voltage control.

10. (Previously added) The control circuit according to claim 9, wherein each of the drive transistor, the address transistor and the photosensitive transistor comprises a thin film transistor.

11. (Previously added) The control circuit according to claim 9, wherein each of the drive transistor, the address transistor and the photosensitive transistor comprises a p-type transistor.

12. (Previously added) The control circuit according to claim 9, wherein each of the drive transistor, the address transistor and the photosensitive transistor comprises an n-type transistor.

13. (Previously added) The control circuit according to claim 9, wherein the first selection line comprises a first row line.

14. (Previously added) The control circuit according to claim 13, wherein the second selection line, comprises a second row line set to a separate voltage.

15. (Previously added) The control circuit according to claim 9, wherein the second selection line is individually addressable.

16. (Previously added) The control circuit according to claim 9, wherein the second selection line is formed by a single common terminal.

17. (Previously added) A control circuit for controlling a display element of a pixel in a pixel array, the control circuit comprising:

a first transistor for driving the display element, the first transistor being connected in series between a common current line and a common voltage supply line of the pixel array;

a second transistor for applying a data signal to the drive transistor, a gate

of the second transistor being connected to a row selection line;

a capacitor connected to a gate of the first transistor; and

a third transistor coupled to the capacitor to regulate a charge stored on the capacitor in accordance with light received by the third transistor from the display element, a gate of the third transistor being connected to a separate source voltage.